

[VistaStudio/VistaCube] Description of Config file.

Published by **Jin Park** on October 25, 2017

This post describes what is Config file and what is included in it.

Definition :

Config file named **"fw_rdc200.cfg"** is a set of parameters/flags that are used in firmware of RDC200.

If this config file is NOT downloaded or NOT matched with Hardware display module(RDC200 and panel configuration), *display part can't work normally.*

Full mandatory download files are described here as **"Which files should be downloaded for display?"**

Config file can be provided as below method :

If you use Vista Studio™, this file is automatically generated.

- Items related to bootlogo or debug message should be modified directly.

If you use Vista Cube™, you should create or modify manually all items from template file.

- e.g. A RDC200/RDP550F template file is in
\$RDC200_INIT_PATH\download\rdp550f/fw_rdc200.cfg

Two use cases :

Use Case 1 : If you want to place bootlogo image on the top-center of the screen,

- Edit **pox_x = 900** and **pox_y = 10** in a fw_rdcx00.config file.
- Download it via VistStudio™ or VistaCube™ (you can download only one config item)
- Reboot.

Use Case 2 : If you want to display debug messages to the serial port,

- Edit **debug_en = 1** in a fw_rdcx00.config file.
- Download it via VistStudio™ or VistaCube™ (you can download only one config item).
- Reboot.
- Connect Uart serial port of RDC to your Serial port.
 - RXD/TXD singal is LVCMOS level.
 - Baudrate : 14400

Item description :

panel_model	0 : RDP501H 1 : RDP360H 2 : RDP550F
panel_0_attached	true : if RDP is attached to PORT 0 false : if RDP is not attached to PORT 0
I2C_addr_panel_0	I2C Slave Address for panel_0
panel_1_attached	true : if RDP is attached to PORT 1 false : if RDP panel is not attached to PORT 1
I2C_addr_panel_1	I2C Slave Address for panel_1
rdc_init_sel	0 : Only init file 1 : Init file with config file
input_resolution	0 : Manual (Use LUT) 1 : HD 2 : FHD 3 : 2K1K 4 : QHD 5 : 2K2K 6 : WQXGA 7 : HD3D
output_resolution	0 : Manual (Use LUT) 1 : HD 2 : FHD 3 : 2K1K 4 : QHD 5 : 2K2K 6 : WQXGA 7 : HD3D
dif_path	0 : LVDS 1 : LVCMOS 2 : MIPI 3 : HDMI 4 : Pattern Generator 5 : Input path disable
lvds_pclk	0 : Use port 0 clock 1 : Use port 1 clock
lvds_port	0 : Single pixel mode port 0 1 : Single pixel mode port 1 2 : Dual pixel mode (port 0 is upper) 3 : Dual pixel mode(port 1 is upper)
lvds_rev_bus	0 : Don't reverse both 1 : Reverse port 0 2 : Reverse port 1 3 : Reverse both
mipl_num_lane	0 : Under 500 Mbps 1 : Under 750 Mbps 2 : 864 Mbps 3 : Under 1000 Mbps 4 : Under 1333 Mbps 5 : Under 1500 Mbps
mipl_auto_pd	0 : Power up inactive lanes 1 : Power down inactive lanes
mipl_cont_clk	0 : Non-continuous clock mode 1 : Power down
bootlogo_en	Enable Bootlogo image to display when boot time or no input source. 0 : Disable 1 : Enable
bootlogo_lut	Bootlogo drawer index (Do not modify) 0
mag	Bootlogo magnification 0 : x1 1 : x2 2 : x3 3 : x4
debug_en	Enable logo messages of firmware to be printed via serial com port. 0 Disable 1 Enable
pos_x	Bootlogo X coordinate of starting position to be draw
pos_y	Bootlogo Y coordinate of starting position to be draw

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